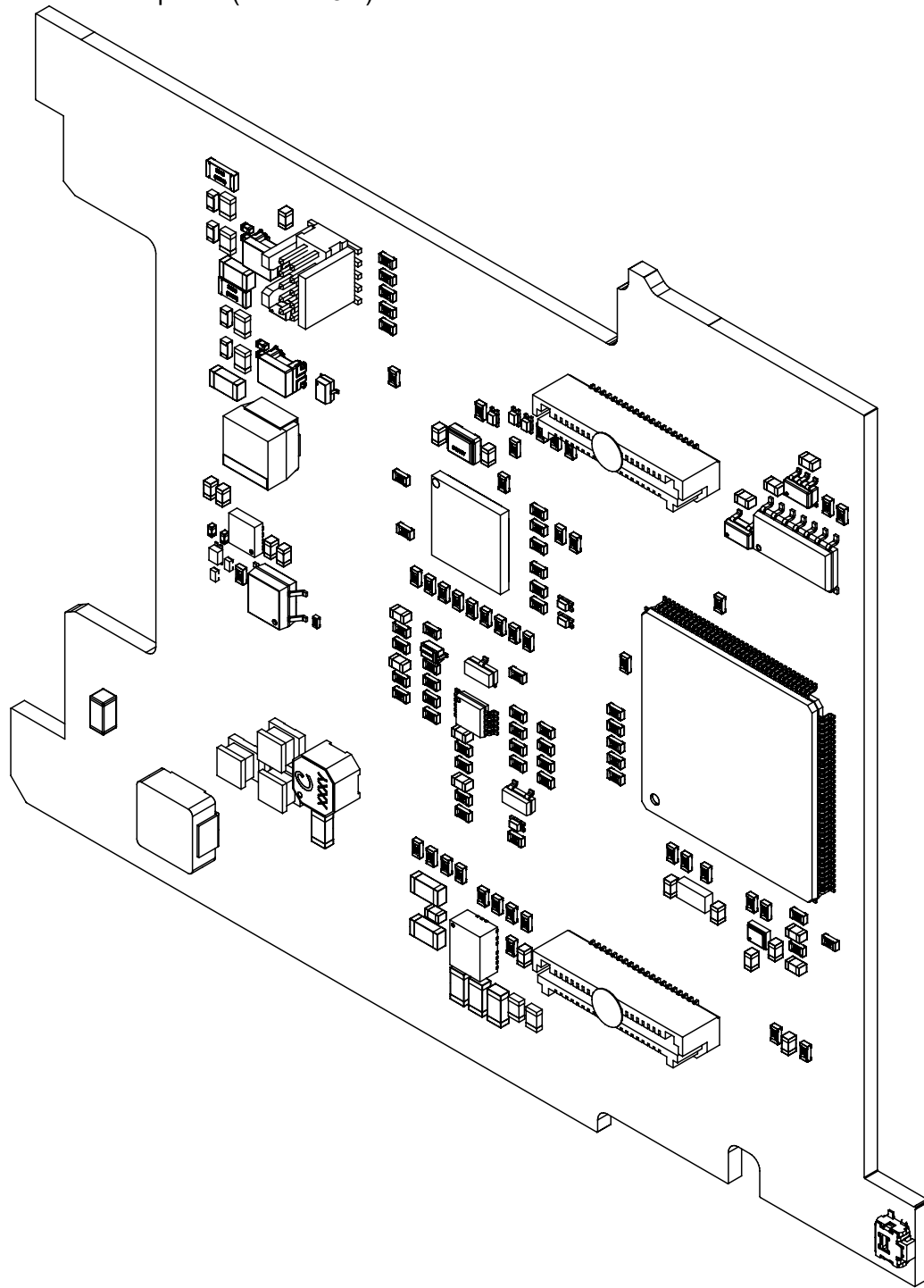
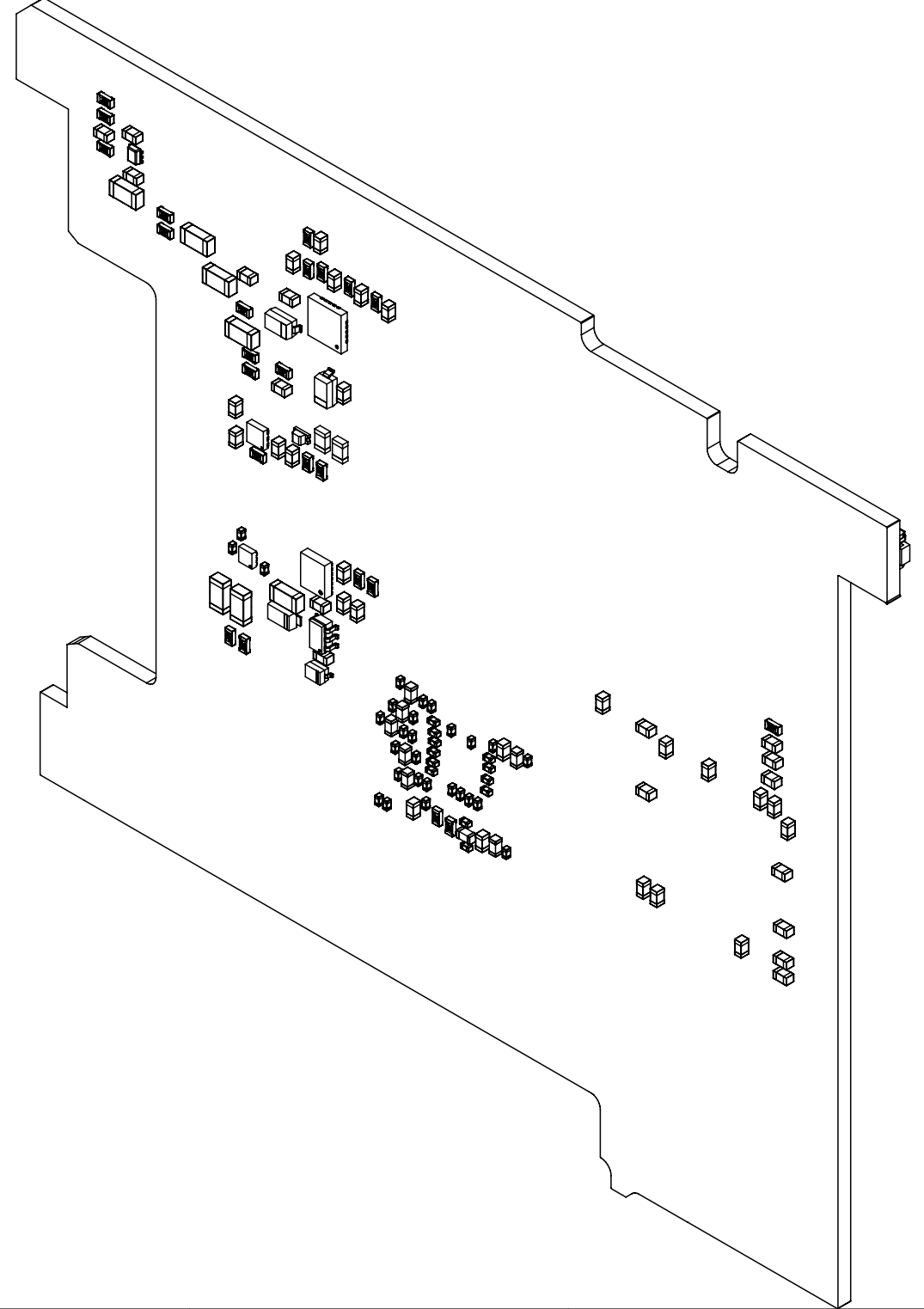


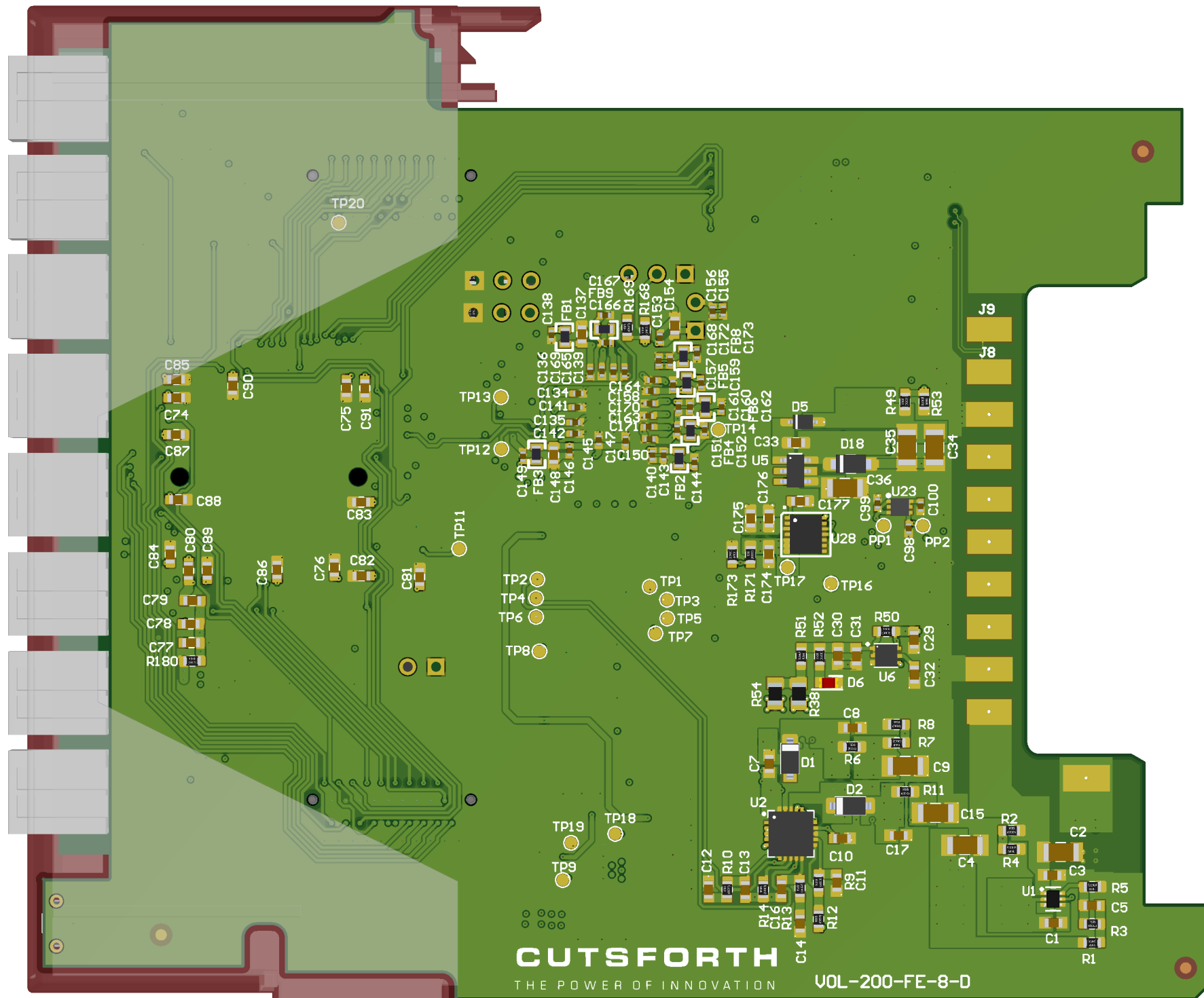
View from Top side (Scale 1.8:1)

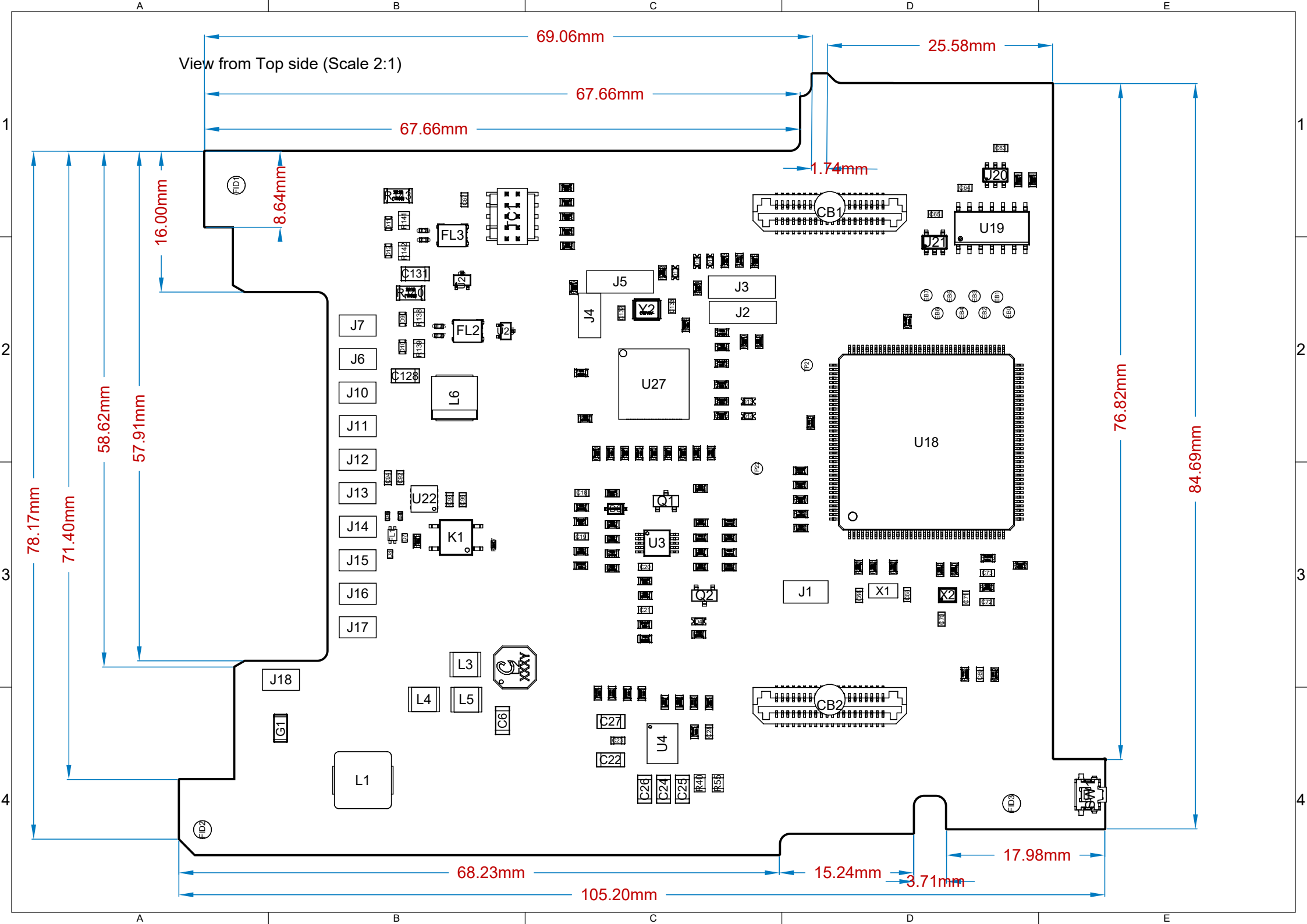


View from Bottom side (Scale 1.8:1)











A

B

C

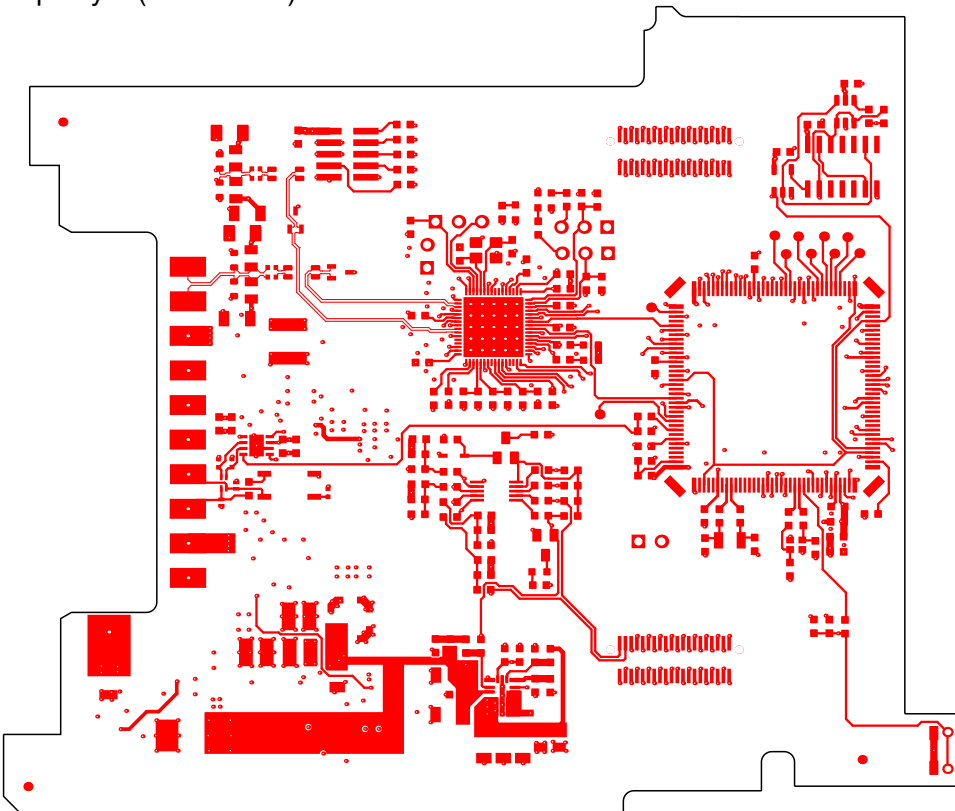
D

E

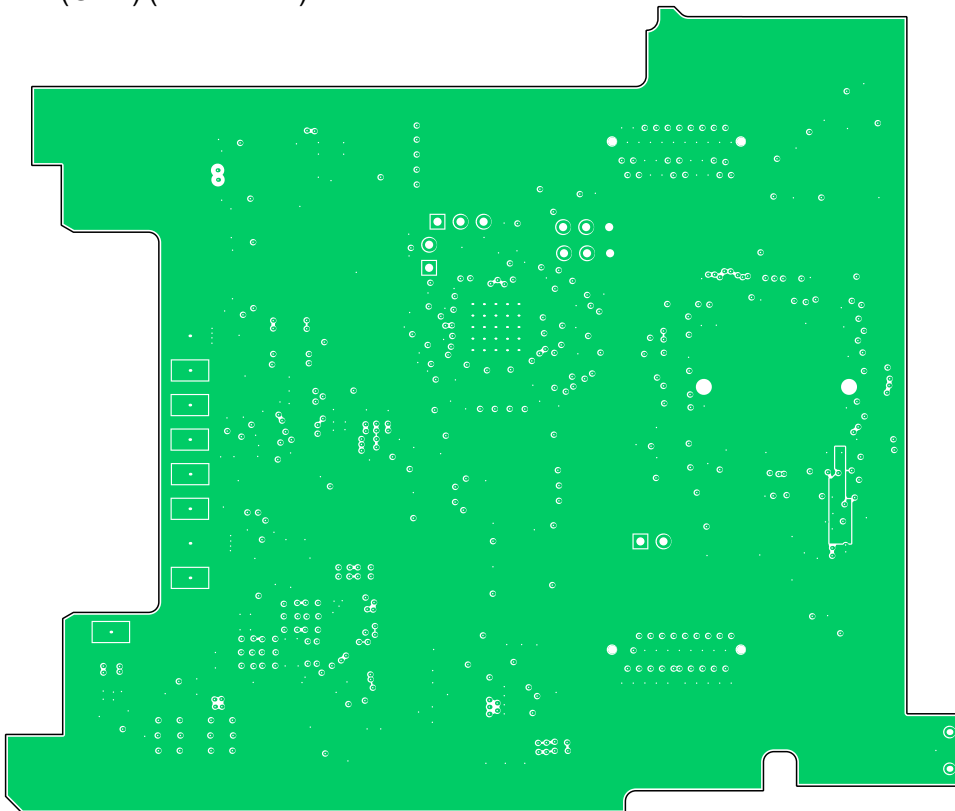
1

1

Top Layer (Scale 1.2:1)



Int1 (GND) (Scale 1.2:1)



2

2

3

3

4

4

A

B

C

D

E

A

B

C

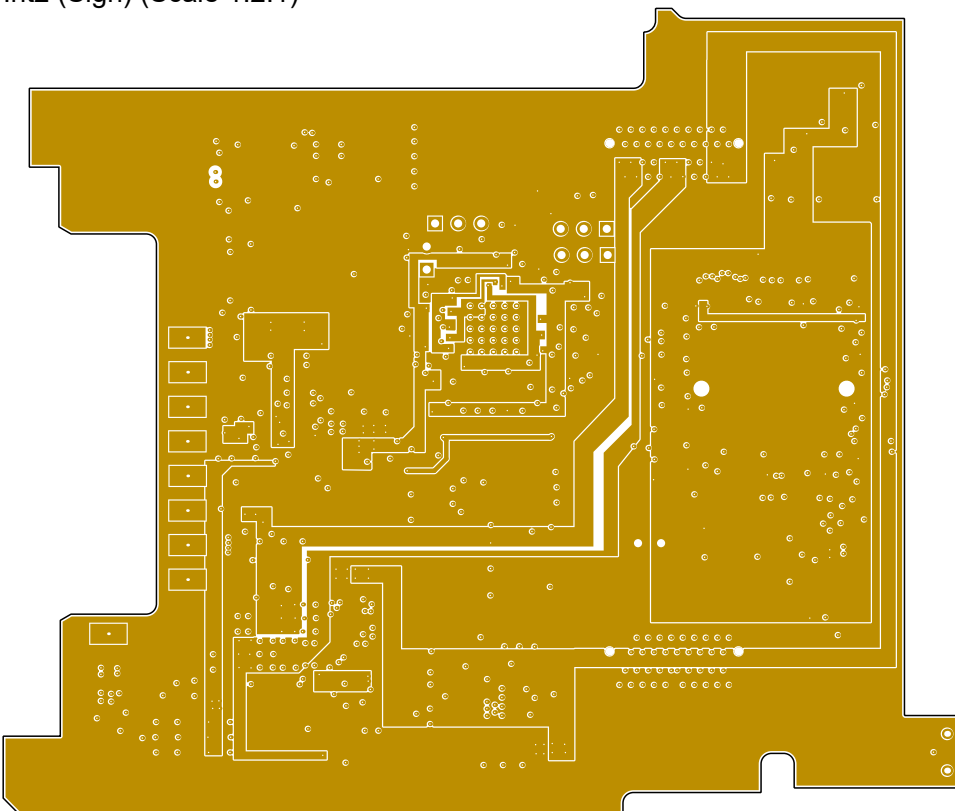
D

E

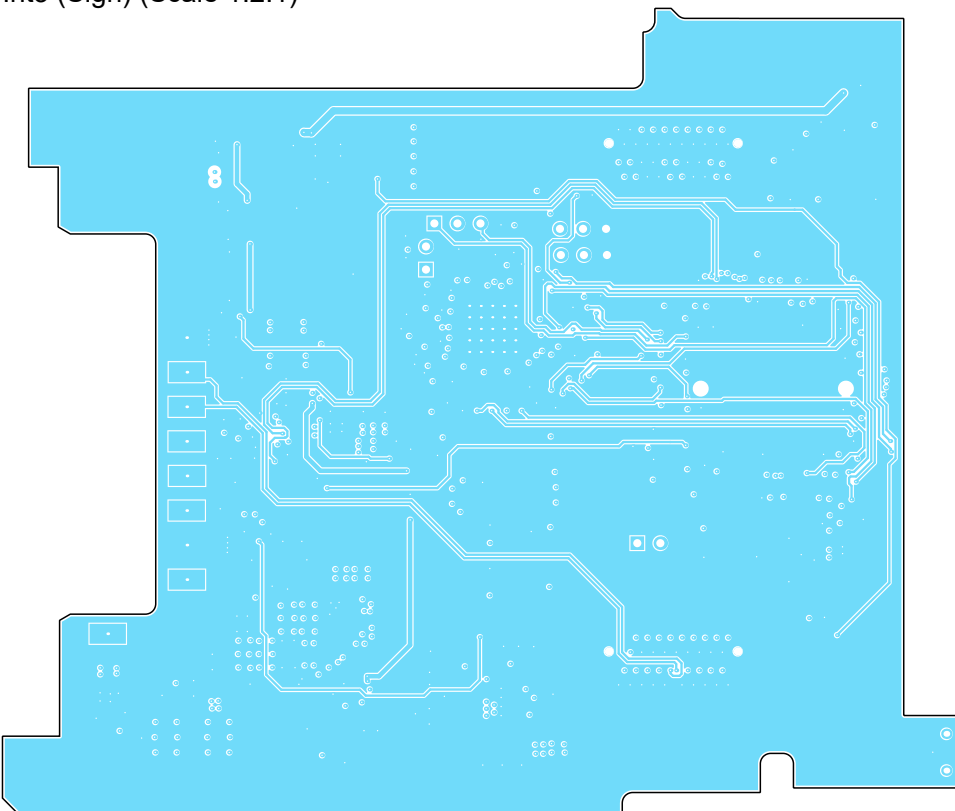
1

1

Int2 (Sign) (Scale 1.2:1)



Int3 (Sign) (Scale 1.2:1)



2

2

3

3

4

4

A

B

C

D

E

A

B

C

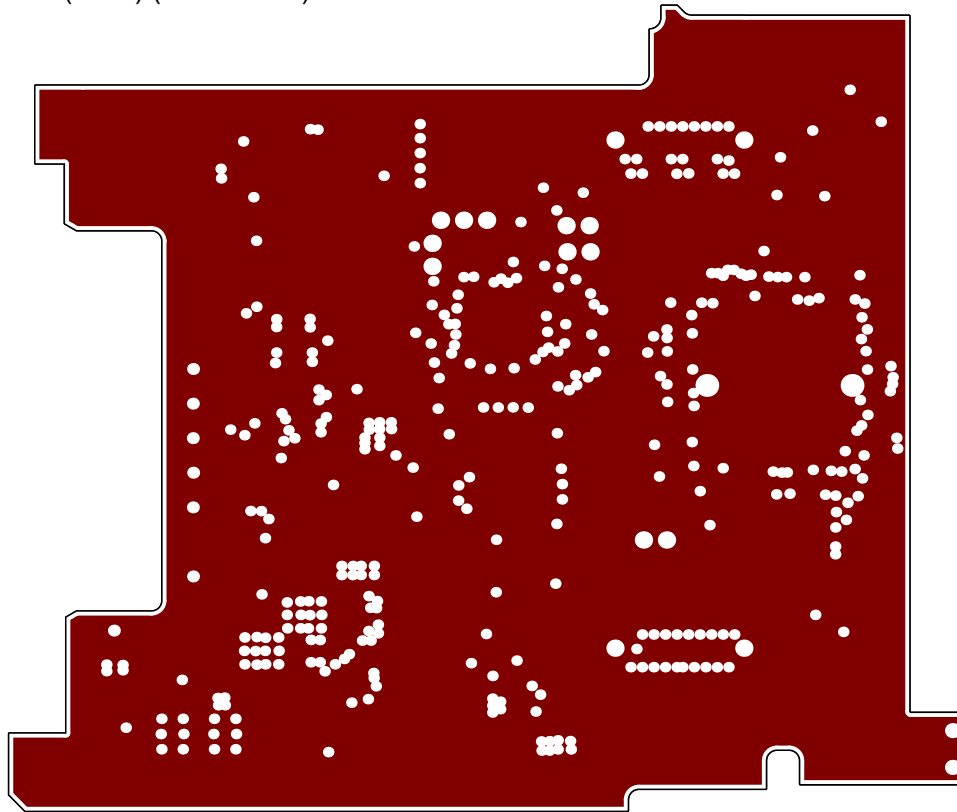
D

E

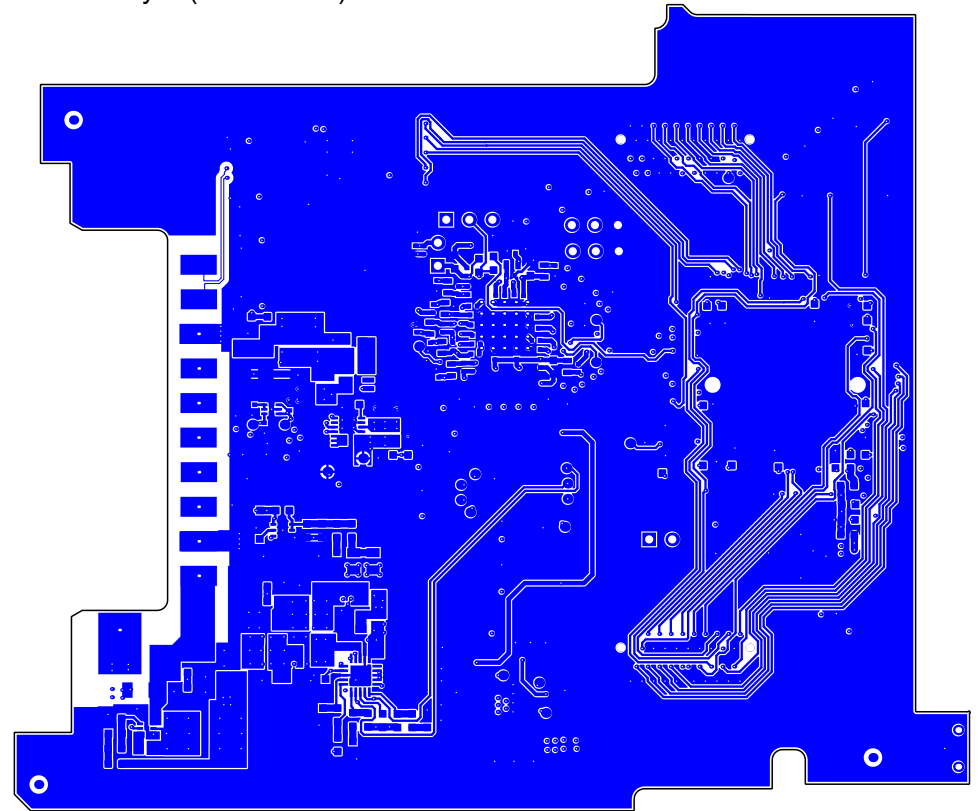
1

1

Int4 (GND) (Scale 1.2:1)



Bottom Layer (Scale 1.2:1)



2

2

3

3

4

4

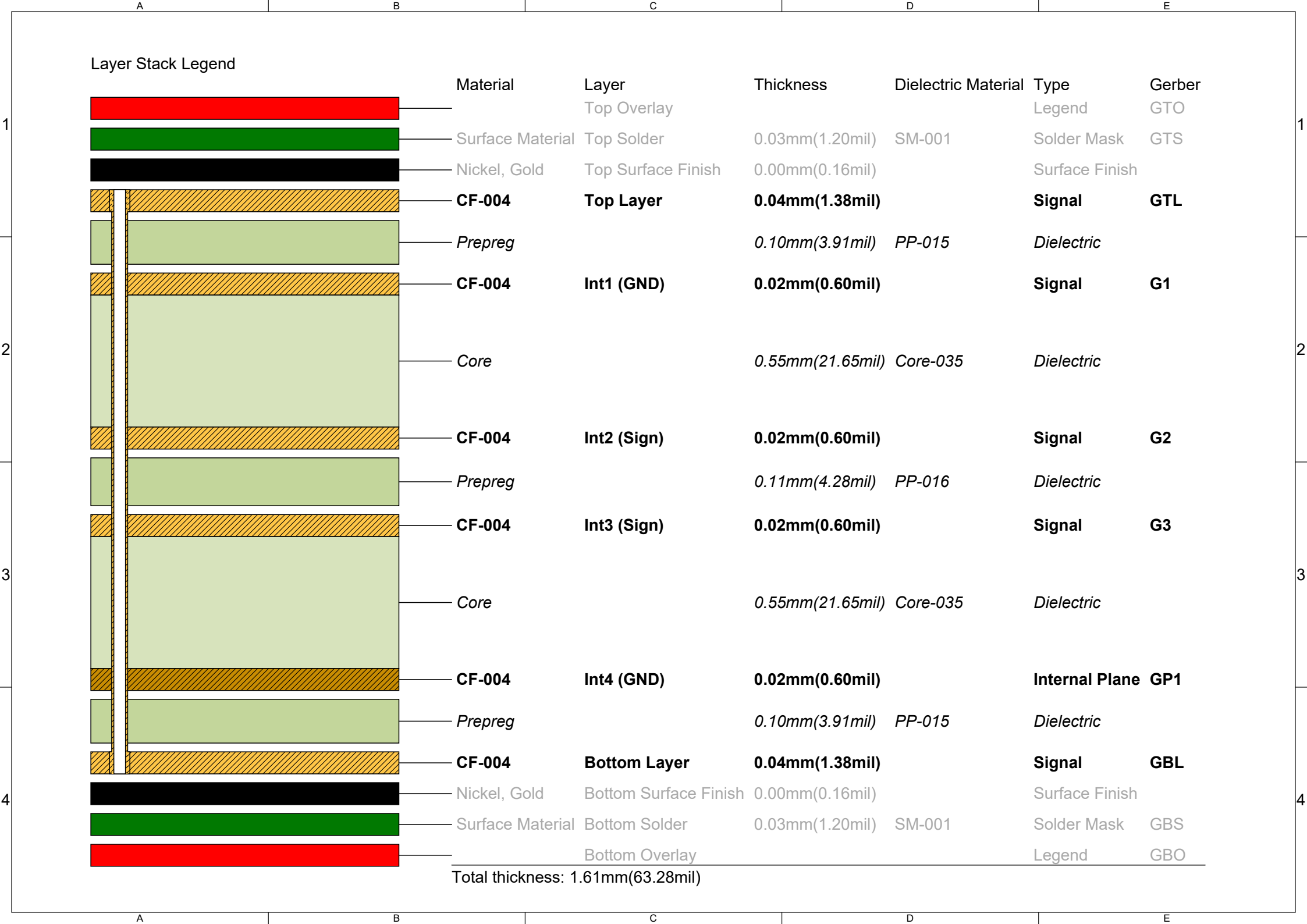
A

B

C

D

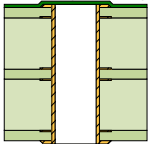
E



Transmission Line Structure Table

| Impedance Id | Transmission Line              | Target Impedance | Calculated Impedance | Trace layer  | Wide Trace Width | Narrow Trace Width | Gap    | Reference layers       | Substack          | Clearance | Target Tolerance |
|--------------|--------------------------------|------------------|----------------------|--------------|------------------|--------------------|--------|------------------------|-------------------|-----------|------------------|
| 1            | Coated Microstrip              | 50               | 50.01                | Top Layer    | 0.15mm           | 0.15mm             |        | Int1 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 2            | Edge-Coupled Coated Microstrip | 100              | 94.35                | Top Layer    | 0.12mm           | 0.12mm             | 0.18mm | Int1 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 3            | Edge-Coupled Coated Microstrip | 90               | 85.06                | Top Layer    | 0.14mm           | 0.14mm             | 0.15mm | Int1 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 4            | Edge-Coupled Coated Microstrip | 85               | 81.30                | Top Layer    | 0.16mm           | 0.16mm             | 0.15mm | Int1 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 5            | Offset Stripline               | 50               | 49.99                | Int2 (Sign)  | 0.14mm           | 0.14mm             |        | Int1 (GND),Int3 (Sign) | Board Layer Stack | 0.13mm    | 10%              |
| 6            | Edge-Coupled Offset Stripline  | 100              | 79.88                | Int2 (Sign)  | 0.16mm           | 0.16mm             | 0.14mm | Int1 (GND),Int3 (Sign) | Board Layer Stack | 0.00mm    | 10%              |
| 7            | Edge-Coupled Offset Stripline  | 90               | 69.24                | Int2 (Sign)  | 0.21mm           | 0.21mm             | 0.13mm | Int1 (GND),Int3 (Sign) | Board Layer Stack | 0.00mm    | 10%              |
| 8            | Edge-Coupled Offset Stripline  | 85               | 85.00                | Int2 (Sign)  | 0.14mm           | 0.14mm             | 0.15mm | Int1 (GND),Int3 (Sign) | Board Layer Stack | 0.13mm    | 10%              |
| 9            | Offset Stripline               | 50               | 49.99                | Int3 (Sign)  | 0.14mm           | 0.14mm             |        | Int2 (Sign),Int4 (GND) | Board Layer Stack | 0.13mm    | 10%              |
| 10           | Edge-Coupled Offset Stripline  | 100              | 99.42                | Int3 (Sign)  | 0.16mm           | 0.16mm             | 0.14mm | Int1 (GND),Int4 (GND)  | Board Layer Stack | 0.00mm    | 10%              |
| 11           | Edge-Coupled Offset Stripline  | 90               | 89.57                | Int3 (Sign)  | 0.21mm           | 0.21mm             | 0.13mm | Int1 (GND),Int4 (GND)  | Board Layer Stack | 0.00mm    | 10%              |
| 12           | Edge-Coupled Offset Stripline  | 85               | 84.99                | Int3 (Sign)  | 0.30mm           | 0.30mm             | 0.15mm | Int1 (GND),Int4 (GND)  | Board Layer Stack | 0.13mm    | 10%              |
| 13           | Coated Microstrip              | 50               | 50.01                | Bottom Layer | 0.15mm           | 0.15mm             |        | Int4 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 14           | Edge-Coupled Coated Microstrip | 100              | 94.35                | Bottom Layer | 0.12mm           | 0.12mm             | 0.18mm | Int4 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 15           | Edge-Coupled Coated Microstrip | 90               | 85.06                | Bottom Layer | 0.14mm           | 0.14mm             | 0.15mm | Int4 (GND)             | Board Layer Stack | 0.13mm    | 10%              |
| 16           | Edge-Coupled Coated Microstrip | 85               | 81.30                | Bottom Layer | 0.16mm           | 0.16mm             | 0.15mm | Int4 (GND)             | Board Layer Stack | 0.13mm    | 10%              |

Via Type Type 1A (Scale 12:1)



Drill Table

| Symbol | Count     | Hole Size | Plated     | Hole Tolerance |
|--------|-----------|-----------|------------|----------------|
| ✕      | 579       | 0.15mm    | Plated     |                |
| ⊞      | 25        | 0.20mm    | Plated     |                |
| ⊕      | 9         | 0.30mm    | Plated     |                |
| ⊗      | 2         | 0.65mm    | Plated     |                |
| ◇      | 4         | 0.89mm    | Non-Plated |                |
| □      | 13        | 0.90mm    | Plated     |                |
| ▽      | 2         | 1.50mm    | Non-Plated |                |
|        | 634 Total |           |            |                |