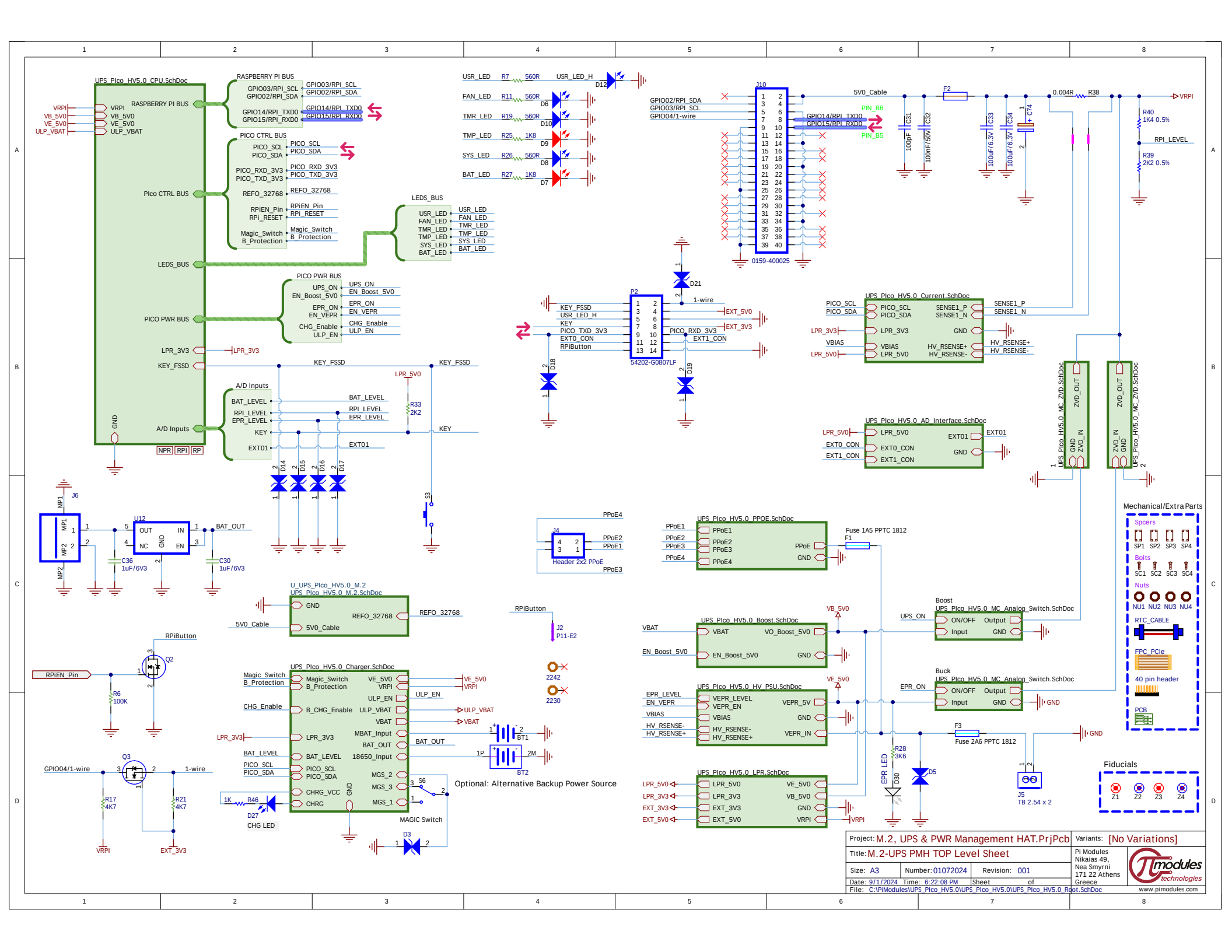
UPS_PIco_HV5.0_CPU.SchDoc
RASPBERRY PI BUS
PICO CTRL BUS
PICO PWR BUS
LEDS_BUS
A/D Inputs
GPIO03/RPI_SCL
GPIO02/RPI_SDA
GPIO14/RPI_TXD0
GPIO15/RPI_RXD0
PICO_SCL
PICO_SDA
PICO_RXD_3V3
PICO_TXD_3V3
REFO_32768
RPiEN_Pin
RPi_RESET
Magic_Switch
B_Protection
UPS_ON
EN_Boost_5V0
EPR_ON
EN_VEPR
CHG_Enable
ULP_EN
USR_LED
FAN_LED
TMR_LED
TMP_LED
SYS_LED
BAT_LED
VRPI
VB_5V0
VE_5V0
ULP_VBAT
LPR_3V3
LPR_5V0
KEY_FSSD
KEY
EXT01
BAT_LEVEL
RPI_LEVEL
EPR_LEVEL
NPR
RPI
RP
J10
0159-400025
5V0_Cable
PIN_B6
PIN_B5
F2
C31
C32
C33
C34
C74
R38
R40
R39
RPI_LEVEL
P2
54202-G0807LF
1-wire
EXT_5V0
EXT_3V3
EXT1_CON
D21
D18
D19
UPS_PIco_HV5.0_Current.SchDoc
SENSE1_P
SENSE1_N
HV_RSENSE+
HV_RSENSE-
VBIAS
UPS_PIco_HV5.0_AD_Interface.SchDoc
EXT0_CON
UPS_PIco_HV5.0_MC_ZVD.SchDoc
ZVD_OUT
ZVD_IN
GND
J6
U12
C36
1uF/6V3
C30
1uF/6V3
BAT_OUT
S3
D14
D15
D16
D17
R33
2K2
PPoE4
PPoE2
PPoE1
PPoE3
Header 2x2 PPoE
J4
U_UPS_PIco_HV5.0_M.2
UPS_PIco_HV5.0_M.2.SchDoc
REFO_32768
5V0_Cable
RPiButton
J2
P11-E2
2242
2230
UPS_PIco_HV5.0_PPOE.SchDoc
Fuse 1A5 PPTC 1812
F1
UPS_PIco_HV5.0_Boost.SchDoc
VBAT
VO_Boost_5V0
EN_Boost_5V0
VB_5V0
Boost
UPS_PIco_HV5.0_MC_Analog_Switch.SchDoc
ON/OFF
Output
Input
UPS_ON
Q2
R6
100K
RPiEN_Pin
Q3
R17
4K7
R21
4K7
GPIO04/1-wire
UPS_PIco_HV5.0_Charger.SchDoc
VE_5V0
VRPI
ULP_EN
ULP_VBAT
VBAT
MBAT_Input
BAT_OUT
18650_Input
MGS_2
MGS_3
MGS_1
CHRG_VCC
CHRG
D27
CHG LED
R46
1K
S6
MAGIC Switch
D3
BT1
BT2
1P
2M
Optional: Alternative Backup Power Source
UPS_PIco_HV5.0_HV_PSU.SchDoc
VEPR_LEVEL
VEPR_EN
VEPR_5V
VEPR_IN
EN_VEPR
HV_RSENSE-
HV_RSENSE+
EPR_ON
Buck
R28
3K6
EPR LED
D30
D5
F3
Fuse 2A6 PPTC 1812
J5
TB 2.54 x 2
UPS_PIco_HV5.0_LPR.SchDoc
LPR_5V0
LPR_3V3
EXT_3V3
EXT_5V0
VE_5V0
VB_5V0
Mechanical/Extra Parts
Spcers
SP1
SP2
SP3
SP4
Bolts
SC1
SC2
SC3
SC4
Nuts
NU1
NU2
NU3
NU4
RTC_CABLE
FPC_PCIe
40 pin header
PCB
Fiducials
Z1
Z2
Z3
Z4
Project: M.2, UPS & PWR Management HAT.PrjPcb
Variants: [No Variations]
Title: M.2-UPS PMH TOP Level Sheet
Size: A3
Number: 01072024
Revision: 001
Date: 9/1/2024 Time: 6:22:08 PM
Sheet of
File: C:\PiModules\UPS_PIco_HV5.0\UPS_PIco_HV5.0\UPS_PIco_HV5.0_Root.SchDoc
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