



# MAX-M10M

**Standard precision GNSS module**

**Professional grade**

Data sheet



## **Abstract**

This data sheet describes the MAX-M10M module, an ultra-low-power GNSS receiver for high-performance asset-tracking applications.

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UBX-22028884 - R03  
C1-Public



## Document information

<b>Title</b>	<b>MAX-M10M</b>	
<b>Subtitle</b>	Standard precision GNSS module	
<b>Document type</b>	Data sheet	
<b>Document number</b>	UBX-22028884	
<b>Revision and date</b>	R03	08-Apr-2024
<b>Disclosure restriction</b>	C1-Public	

<b>Product status</b>	<b>Corresponding content status</b>	
<b>Functional sample</b>	Draft	For functional testing. Revised and supplementary data will be published later.
<b>In development / prototype</b>	Objective specification	Target values. Revised and supplementary data will be published later.
<b>Engineering sample</b>	Advance information	Data based on early testing. Revised and supplementary data will be published later.
<b>Initial production</b>	Early production information	Data from product verification. Revised and supplementary data may be published later.
<b>Mass production / End of life</b>	Production information	Document contains the final product specification.

This document applies to the following products:

<b>Product name</b>	<b>Type number</b>	<b>FW version</b>	<b>IN/PCN reference</b>	<b>Product status</b>
MAX-M10M	MAX-M10M-00B-01	ROM SPG 5.10	UBXDOC-963802114-12186	Mass production

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# 1 Functional description

## 1.1 Overview

MAX-M10M module features the u-blox M10 standard precision GNSS platform and provides exceptional sensitivity and acquisition time for all L1 GNSS signals.

The M10 platform supports concurrent reception of four GNSS (GPS, GLONASS, Galileo, and BeiDou). The high number of visible satellites enables the receiver to select the best signals. This maximizes the position availability, in particular under challenging conditions such as in deep urban canyons.

u-blox Super-S (Super-Signal) technology offers great RF sensitivity and can improve the dynamic position accuracy with small antennas or in non-line-of-sight scenarios.

MAX-M10M is cost and power optimized for designs where a SAW filter and an LNA are integrated in the external active antenna. It works in a wide main supply voltage range of 1.8 - 5 V with an extremely low power consumption of less than 10 mW in a 1 Hz cyclic tracking power save mode.

MAX-M10M offers backwards pin-to-pin compatibility with products from the previous u-blox generations, which saves the designer's effort and reduces costs when upgrading designs to the advanced low-power u-blox M10 GNSS technology.

## 1.2 Performance

Parameter	Specification	Value
Receiver type		u-blox M10 receiver
Accuracy of time pulse signal	RMS	30 ns
	99%	60 ns
Frequency of time pulse signal		Default 1PPS (0.25 Hz to 10 MHz configurable)
Operational limits <sup>1</sup>	Dynamics	≤ 4 g
	Altitude	80,000 m
	Velocity	500 m/s
Velocity accuracy <sup>2</sup>		0.05 m/s
Dynamic heading accuracy <sup>2</sup>		0.3 deg

**Table 1: MAX-M10M specifications**

Parameter		GPS+GAL (default)	GPS+GAL +GLO	GPS+GAL +BDS B1I	GPS+GAL +BDS B1C	GPS+GAL +BDS B1C +GLO
Max navigation update rate <sup>3</sup>	Default	10 Hz	6 Hz	3 Hz	8 Hz	4 Hz
	High performance <sup>4</sup>	20 Hz	16 Hz	12 Hz	16 Hz	10 Hz
Position accuracy (CEP) <sup>5, 6</sup>		1.5 m	1.5 m	1.5 m	1.5 m	1.5 m

<sup>1</sup> Assuming Airborne 4 g platform.

<sup>2</sup> 50% at 30 m/s for dynamic operation.

<sup>3</sup> Minimum 98% fix rate under typical conditions.

<sup>4</sup> Configuration required.

<sup>5</sup> GPS is always in combination with SBAS and QZSS.

<sup>6</sup> CEP, 50%, 24 hours static, -130 dBm, > 6 SVs for each GNSS system.

Parameter		GPS+GAL (default)	GPS+GAL +GLO	GPS+GAL +BDS B1I	GPS+GAL +BDS B1C	GPS+GAL +BDS B1C +GLO
Time To First Fix (TTFF) <sup>5, 7, 8</sup>	Cold start	28 s	23 s	27 s	28 s	23 s
	Hot start	1 s	1 s	1 s	1 s	1 s
	AssistNow Online <sup>9</sup>	1 s	1 s	1 s	1 s	1 s
	AssistNow Offline <sup>10</sup>	2 s	2 s	3 s	2 s	2 s
	AssistNow Autonomous <sup>11</sup>	3 s	4 s	4 s	4 s	4 s
Sensitivity <sup>12</sup>	Tracking and navigation	-165 dBm	-167 dBm	-162 dBm	-163 dBm	-163 dBm
	Reacquisition	-160 dBm	-160 dBm	-160 dBm	-160 dBm	-160 dBm
	Cold Start	-148 dBm	-148 dBm	-148 dBm	-148 dBm	-148 dBm
	Hot start <sup>7</sup>	-159 dBm	-159 dBm	-159 dBm	-159 dBm	-159 dBm

**Table 2: MAX-M10M typical performance in multi-constellation GNSS modes.**

Parameter		GPS	GLONASS	BDS B1I	GALILEO	BDS B1C
Max navigation update rate <sup>3</sup>	Default	18 Hz	18 Hz	18 Hz	18 Hz	18 Hz
	High performance <sup>4</sup>	25 Hz	25 Hz	25 Hz	25 Hz	25 Hz
Position accuracy (CEP) <sup>5, 6</sup>		1.5 m	4 m	2 m	3 m	2 m
Time To First Fix (TTFF) <sup>5, 7, 8</sup>	Cold start	29 s	27 s	30 s	41 s	58 s
	Hot start	1 s	1 s	1 s	1 s	1 s
	AssistNow Online <sup>9</sup>	1 s	1 s	2 s	7 s	N/A
Sensitivity <sup>12</sup>	Tracking and navigation	-165 dBm	-165 dBm	-159 dBm	-157 dBm	-159 dBm
	Reacquisition	-160 dBm	-158 dBm	-158 dBm	-155 dBm	-156 dBm
	Cold Start	-148 dBm	-147 dBm	-144 dBm	-139 dBm	-134 dBm
	Hot start <sup>7</sup>	-159 dBm	-159 dBm	-159 dBm	-155 dBm	-157 dBm

**Table 3: MAX-M10M typical performance in single-GNSS modes**

## 1.3 Supported GNSS constellations

MAX-M10M is a concurrent GNSS receiver that can receive and track multiple GNSS systems. The single RF front-end architecture enables concurrent reception of multiple GNSS constellations. The receiver can be configured for a subset of GNSS constellations to achieve lower power consumption.

The default configuration on MAX-M10M is concurrent reception of GPS and Galileo with QZSS and SBAS enabled.

The following GNSS and their signals are supported:

System	Signals
GPS / QZSS	L1C/A (1575.42 MHz)
Galileo	E1-B/C (1575.42 MHz)

<sup>7</sup> Commanded starts.

<sup>8</sup> All satellites at -130 dBm. Measured at room temperature.

<sup>9</sup> Dependent on the speed and latency of the aiding data connection, commanded starts.

<sup>10</sup> Using seven days old AssistNow Offline data. External memory may be required.

<sup>11</sup> Using two days old orbital predicted data. External memory may be required.

<sup>12</sup> Demonstrated with a good external LNA. Measured at room temperature.

System	Signals
GLONASS	L1OF (1602 MHz + $k \cdot 562.5$ kHz, $k = -7, \dots, 5, 6$ )
BeiDou <sup>13</sup>	B1I (1561.098 MHz), B1C (1575.42 MHz)

**Table 4: Supported GNSS and signals on MAX-M10M**

The following GNSS assistance services are supported:

Service	Support
AssistNow™ Online	GPS L1C/A, Galileo E1, QZSS L1C/A, GLONASS L1OF, BeiDou B1I
AssistNow™ Offline	GPS L1C/A, Galileo E1, GLONASS L1OF
AssistNow™ Autonomous	GPS L1C/A, Galileo E1, GLONASS L1OF, QZSS L1C/A, BeiDou B1I

**Table 5: Supported Assisted GNSS (A-GNSS) services**

The following augmentation systems are supported:

System	Support
SBAS	EGNOS, GAGAN, MSAS, WAAS and BDSBAS
QZSS	L1S (SLAS)

**Table 6: Supported augmentation systems**

The augmentation systems SBAS and QZSS can be enabled only if GPS operation is also enabled.

## 1.4 Supported protocols

MAX-M10M supports the following interface protocols:

Protocol	Type
UBX	Input/output, binary, u-blox proprietary
NMEA versions 2.1, 2.3, 4.0, 4.10 and 4.11 (default)	Input/output, ASCII

**Table 7: Supported protocols**

## 1.5 Firmware features

Feature	Description
Antenna supervisor <sup>14</sup>	Antenna supervisor for active antenna control and short detection
CloudLocate GNSS	Extends the life of energy-constrained IoT applications. Small payload messages supported.
Assisted GNSS	AssistNow Online, AssistNow Offline and AssistNow Autonomous
Backup modes	Hardware backup mode and software standby mode
Power save modes <sup>15</sup>	On/off, cyclic tracking
Super-S	Improved dynamic position accuracy with small antennas
Protection level	Real-time position accuracy estimate with 95% confidence level <sup>16</sup>
Galileo return link messages	Galileo search and rescue (SAR) return link messages (RLM) via Galileo satellite signal

<sup>13</sup> BeiDou B1I cannot be enabled simultaneously with BeiDou B1C or GLONASS L1OF.

<sup>14</sup> External components required, some pins need to be reconfigured.

<sup>15</sup> The power save modes are not available if BeiDou B1C is enabled.

<sup>16</sup> Verified for automotive environment only.

Feature	Description
Data batching	Autonomous tracking up to 10 minutes at 1 Hz
Odometer	Measure traveled distance with support for different user profiles

**Table 8: Firmware features**

Feature	Description
Anti-jamming	RF interference and jamming detection and reporting
Anti-spoofing	Spoofing detection and reporting
Configuration lockdown	Receiver configuration can be locked by command
Message integrity	All messages can be cryptographically signed
Secure boot	Only signed firmware images are executed

**Table 9: Security features**

## 2 System description

### 2.1 Block diagram

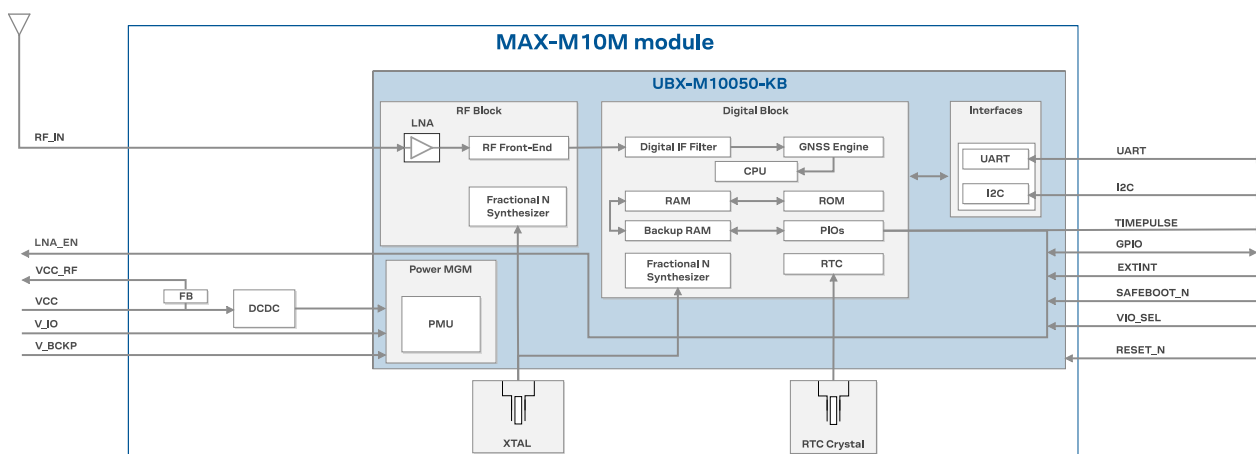


Figure 1: MAX-M10M block diagram



## 3 Pin definition

### 3.1 Pin assignment

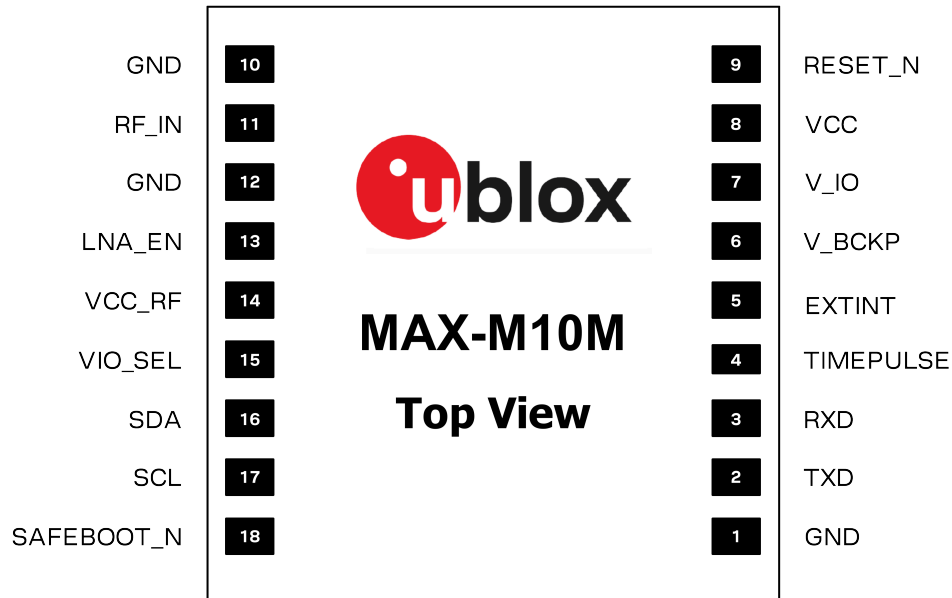


Figure 2: MAX-M10M pin assignment

Pin no.	Name	PIO no.	I/O	Description
1	GND	-	-	Connect to GND
2	TXD	1	O	UART TX. Leave open if not used.
3	RXD	0	I	UART RX. Leave open if not used.
4	TIMEPULSE	4	O	Time pulse signal (shared with SAFEBOOT_N pin) <sup>17</sup>
5	EXTINT	5	I	External interrupt. Leave open if not used.
6	V_BCKP	-	I	Backup voltage supply
7	V_IO	-	I	IO voltage supply
8	VCC	-	I	Main voltage supply
9	RESET_N	-	I	System reset (active low). Has to be low for at least 1 ms to trigger a reset.
10	GND	-	-	Connect to GND
11	RF_IN	-	I	GNSS signal input
12	GND	-	-	Connect to GND
13	LNA_EN	-	O	On/Off external LNA or active antenna
14	VCC_RF	-	O	Output voltage RF section
15	VIO_SEL	-	I	Voltage selector for V_IO supply. Connect to GND for 1.8 V supply, or leave open for 3.3 V supply.
16	SDA	2	I/O	I2C data. Leave open if not used.
17	SCL	3	I	I2C clock. Leave open if not used.
18	SAFEBOOT_N	-	I	Safeboot mode (active low). Leave open if not used. <sup>17</sup>

Table 10: MAX-M10M pin assignment

<sup>17</sup> The receiver enters safeboot mode if this pin is low at start up. The SAFEBOOT\_N pin is internally connected to TIMEPULSE pin through a 1 kΩ series resistor.

## 3.2 Pin state

Table 11 defines the state of the PIOs and RESET\_N pins in different modes. The functions of the PIOs are as defined in the default configuration.

PIO no.	Pin no.	Default function	Continuous mode	Software standby mode	Safe boot mode
0	3	RXD	Input pull-up	Input pull-up	Input pull-up
1	2	TXD	Output	Input pull-up	Output
2	16	SDA	Input pull-up	Input pull-up	Input pull-up
3	17	SCL	Input pull-up	Input pull-up	Input pull-up
4 <sup>17</sup>	18	SAFEBOOT_N	Output	Input pull-up	Output (low)
	4	TIMEPULSE	Output	Input pull-up	Output (low)
5	5	EXTINT	Input pull-up	Input pull-up	Input pull-up
7	13	LNA_EN	Output (high)	Input pull-down	Input pull-up
-	9	RESET_N	Input pull-up	Input pull-up	Input pull-up

**Table 11: Pins state**





In reset mode (RESET\_N = low), all PIOs are configured as input pull-up.



In hardware backup mode (VCC = 0 V and V\_IO = 0 V), PIOs must not be driven.

## 4 Electrical specifications

### 4.1 Absolute maximum ratings


-  CAUTION. Risk of device damage. Exceeding the absolute maximum ratings may affect the lifetime and reliability of the device or permanently damage it. Do not exceed the absolute maximum ratings.
-  This product is not protected against overvoltage or reversed voltages. Use appropriate protection to avoid device damage from voltage spikes exceeding the specified boundaries.


Symbol	Parameter	Min	Max	Unit
VCC	Main supply voltage	-0.3	6	V
V <sub>IO</sub>	IO supply voltage, VIO_SEL = GND.	-0.3	1.98	V
	IO supply voltage, VIO_SEL = open.	-0.3	3.6	V
	Voltage ramp on V <sub>IO</sub> <sup>18</sup>	25	35000	µs/V
V <sub>BCKP</sub>	Backup supply voltage	-0.3	3.6	V
V <sub>PIO</sub>	Input voltage on RESET_N and digital pins VIO_SEL = GND	-0.3	V <sub>IO</sub> + 0.3 (max 1.98)	V
	Input voltage on RESET_N and digital pins VIO_SEL = open.	-0.3	V <sub>IO</sub> + 0.3 (max 3.6)	V
I <sub>PIO</sub>	Max source / sink current, digital pins <sup>19</sup>	-10	10	mA
ICC <sub>RF</sub>	Max source current, VCC <sub>RF</sub>		250	mA
P <sub>rfin</sub>	RF input power at RF_IN <sup>20</sup>		+15	dBm
T <sub>amb</sub>	Ambient temperature	-40	+85	°C
T <sub>s</sub>	Storage temperature	-40	+85	°C

**Table 12: Absolute maximum ratings**

### 4.2 Operating conditions

Table 13 shows the general operating conditions. Table 14 shows the electrical parameters for digital I/O.

-  The V<sub>IO</sub> voltage range is selected with the VIO\_SEL pin.

-  For designs with 1.8 V supply at V<sub>IO</sub> and V<sub>BCKP</sub> supplied, switch off V<sub>IO</sub> supply 100 ms before VCC when transitioning to hardware backup mode. Alternatively, put the receiver to software standby mode by sending UBX-RXM-PMREQ message before switching off V<sub>IO</sub> and VCC. For designs with 3 V supplies, both supplies can be switched off simultaneously or ensure that V<sub>IO</sub> is switched off before VCC.

Symbol	Parameter	Min	Typical	Max	Unit
VCC	Main supply voltage	1.76	1.8, 3.3	5.5	V
V <sub>IO</sub>	IO supply voltage, VIO_SEL = GND	1.68	1.8	1.98	V
	IO supply voltage, VIO_SEL = open	2.7	3.3	3.6	V
V <sub>BCKP</sub>	Supply voltage, backup domain	1.65		3.6	V

<sup>18</sup> Exceeding the voltage ramp speed may permanently damage the device.

<sup>19</sup> The SAFEBOOT\_N pin has an internal 1 kΩ series resistor.

<sup>20</sup> Test conditions: source impedance = 50 Ω, continuous wave.

Symbol	Parameter	Min	Typical	Max	Unit
V <sub>IOSWITCH</sub>	V <sub>IO</sub> voltage threshold to switch an internal supply for the backup domain from V <sub>IO</sub> to V <sub>BCKP</sub>		1.45		V
VCC <sub>RF</sub>	VCC <sub>RF</sub> output voltage		VCC - 0.1		V
ICC <sub>RF</sub>	VCC <sub>RF</sub> output current			50	mA
Z <sub>in</sub> <sup>21</sup>	Input impedance at RF_IN		50		Ω
NF <sub>tot</sub>	Receiver chain noise figure		3.5		dB
Ext_gain <sup>22</sup>	External gain at RF_IN, normal gain mode (default)			40	dB
	External gain at RF_IN, low gain mode	14		50	dB
	External gain at RF_IN, bypass mode	23		60	dB
T <sub>opr</sub>	Operating temperature	-40		+85	°C

**Table 13: General operating conditions**

Symbol	Parameter	Min	Typical	Max	Unit
I <sub>leak</sub>	Leakage current input pins <sup>23</sup>		25		nA
V <sub>in</sub>	Input pin voltage range	0		V <sub>IO</sub>	V
V <sub>il</sub>	Low-level input voltage			0.63	V
V <sub>ih</sub>	High-level input voltage	0.68 x V <sub>IO</sub>			V
V <sub>ol</sub>	Low-level output voltage, I <sub>out</sub> = -2 mA <sup>24</sup>			0.4	V
V <sub>oh</sub>	High-level output voltage, I <sub>out</sub> = 2 mA <sup>24</sup>	V <sub>IO</sub> - 0.4			V
R <sub>pu, IO</sub>	Pull-up resistance, Digital IO <sup>25</sup> . VIO_SEL = GND	6	17	72	kΩ
R <sub>pu, IO</sub>	Pull-up resistance, Digital IO <sup>25</sup> . VIO_SEL = open	8	18	40	kΩ
R <sub>pd, IO</sub>	Pull-down resistance, Digital IO	21	80	180	kΩ
R <sub>pu, SAFEBOOT_N</sub>	Pull-up resistance, SAFEBOOT_N <sup>26</sup>	6	17	72	kΩ
R <sub>pu, RESET_N</sub>	Pull-up resistance, RESET_N	7	10	13	kΩ

**Table 14: Digital IO**

## 4.3 Indicative power requirements

This section provides examples of typical current requirements. They are characterized on samples using a cold start command. The actual power requirements may vary depending on the firmware version used, the external circuitry, the number of satellites tracked, the signal strength, the type and time of start, duration, internal LNA gain mode, and the test conditions.

All values in [Table 15](#), [Table 16](#), and [Table 17](#) have been measured at 25 °C ambient temperature with the default configuration unless otherwise stated. SBAS and QZSS are active in all measurements.

[Table 15](#) shows indicative current consumption for VCC and V<sub>IO</sub> with a 3.0 V supply.

<sup>21</sup> The RF\_IN input integrates a built-in DC block.

<sup>22</sup> The internal LNA gain is configurable.

<sup>23</sup> V<sub>in</sub> = V<sub>IO</sub>, at room temperature.

<sup>24</sup> TIMEPULSE (PIO4) has 4 mA current drive/sink capability.

<sup>25</sup> TXD, RXD, TIMEPULSE, EXTINT, SCL, SDA, and LNA\_EN.

<sup>26</sup> The SAFEBOOT\_N pin has an additional 1 kΩ series resistor.

Symbol (Parameter)	Conditions	GPS	GPS+GAL (default)	GPS+GAL +GLO	GPS+GAL +BDS B1I	GPS+GAL +BDS B1C	GPS+GAL +BDS B1C +GLO	Unit
$I_{VCC}^{27, 28}$ (Current at VCC)	Acquisition <sup>29</sup>	6	8	10	9.5	9	11	mA
	Tracking (Continuous mode)	5	5.5	6.5	7	6	7.5	mA
	Tracking (Power save mode) <sup>30</sup>	2	2.1	2.5	2.5	-	-	mA
$I_{V_{IO}}^{27}$ (Current at V <sub>IO</sub> )	Acquisition and Tracking (Continuous mode)	1.7	1.7	1.8	1.7	1.7	1.8	mA
	Tracking	1.5	1.5	1.5	1.5	-	-	mA
	(Power save mode) <sup>30</sup>	1.5	1.5	1.5	1.5	-	-	mA

**Table 15: Typical currents for 3.0 V supply at VCC and V<sub>IO</sub>**

Table 16 shows indicative current consumption for VCC and V<sub>IO</sub> with a 1.8 V supply.

Symbol (Parameter)	Conditions	GPS	GPS+GAL (default)	GPS+GAL +GLO	GPS+GAL +BDS B1I	GPS+GAL +BDS B1C	GPS+GAL +BDS B1C +GLO	Unit
$I_{VCC}^{27, 28}$ (Current at VCC)	Acquisition <sup>29</sup>	10	13.5	16	15.5	15	18	mA
	Tracking (Continuous mode)	8	9	11	11.5	10	12	mA
	Tracking (Power save mode) <sup>30</sup>	3.2	3.5	4.2	4.2	-	-	mA
$I_{V_{IO}}^{27}$ (Current at V <sub>IO</sub> )	Acquisition and Tracking (Continuous mode)	1.7	1.7	1.8	1.7	1.7	1.8	mA
	Tracking	1.5	1.5	1.5	1.5	-	-	mA
	(Power save mode) <sup>30</sup>	1.5	1.5	1.5	1.5	-	-	mA

**Table 16: Typical currents for 1.8 V supply at VCC and V<sub>IO</sub>**


The inrush current can go up to 100 mA at startup. Ensure that the external power supply is able to deliver up to 100 mA.

Table 17 shows current consumption for the backup modes.

Symbol	Parameter	Conditions	Typ.	Unit
$I_{V_{BCKP}}^{31}$	Total current in hardware backup mode	V <sub>BCKP</sub> = 3.3 V, V <sub>IO</sub> = VCC = 0 V	28	μA
$I_{V_{IO}}$	V <sub>IO</sub> current in software standby mode	V <sub>IO</sub> = 1.8 V	37	μA
		V <sub>IO</sub> = 3.3 V	46	μA
$I_{VCC}$	VCC current in software standby mode	VCC = 3.3 V	120	nA

**Table 17: Backup currents**


Extreme operating temperatures can significantly impact the specified values. If an application operates near the min or max temperature limits, ensure the specified values are not exceeded.

<sup>27</sup> 1 Hz navigation update rate.

<sup>28</sup> Internal LNA set to normal gain. Simulated signal using power levels of -130 dBm.

<sup>29</sup> Average current from start-up until the first fix.

<sup>30</sup> Cyclic tracking operation. BeiDou B1C is not supported in this mode.

<sup>31</sup>  $I_{V_{BCKP}}$  current in normal operation (V<sub>BCKP</sub> = 3.3 V, V<sub>IO</sub> = VCC = 3.3V) is ~3 μA.

## 5 Communication interfaces

The receiver supports communication over the UART and I2C interfaces.

All the inputs have an internal pull-up resistor in normal operation and can be left open if not used. The voltage level at the PIO pins is related to the V<sub>IO</sub> supply voltage.

### 5.1 UART

The UART interface supports configurable baud rates. Hardware flow control is not supported. UART specifications are described in [Table 18](#).

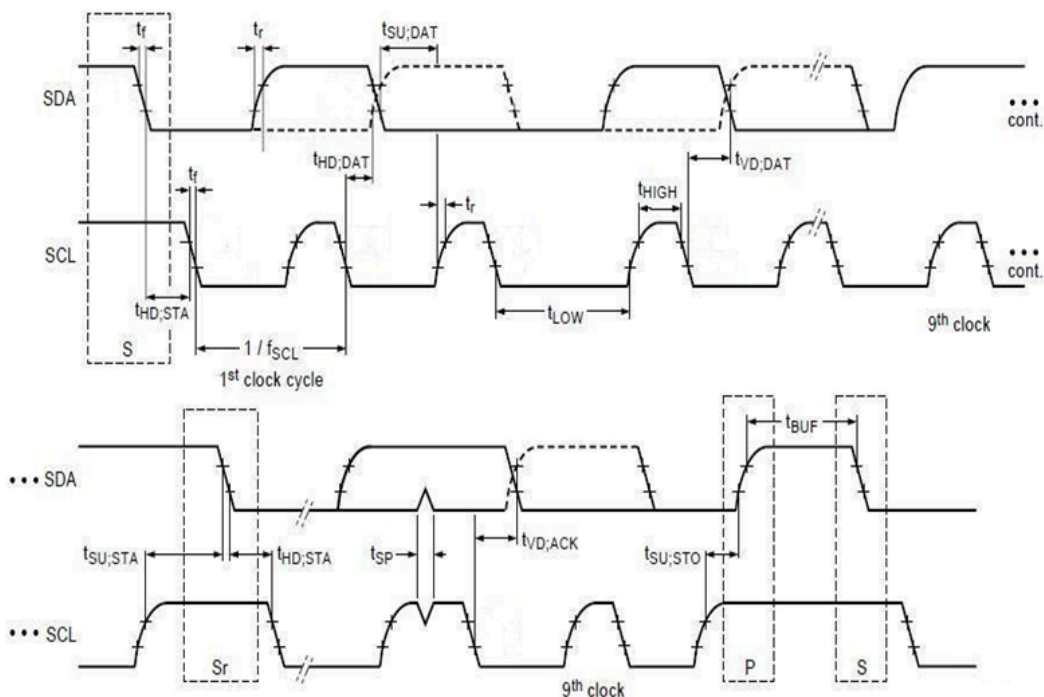
Symbol	Parameter	Min	Max	Unit
R <sub>u</sub>	Baud rate	9600	921600	bit/s
Δ <sub>Tx</sub>	Tx baud rate accuracy	-1%	+1%	-
Δ <sub>Rx</sub>	Rx baud rate tolerance	-2.5%	+2.5%	-

**Table 18: UART specifications**

### 5.2 I2C

An I2C interface is available for communication with an external host CPU in the I2C Fast-mode. Backwards compatibility with the Standard-mode I2C bus operation is not supported. The interface can be operated only in the peripheral mode with a maximum clock frequency of 320 kHz<sup>32</sup>.

The interface can make use of clock stretching by holding the SCL line LOW to pause a transaction. In this case, the bit transfer rate is reduced. The maximum clock stretching time is 20 ms.



**Figure 3: I2C peripheral specification**

<sup>32</sup> External pull-up resistors may be needed to achieve 320 kbit/s communication speed, as the internal pull-up resistance can be very large.

Symbol	Parameter	I2C Fast-mode		Unit
		Min	Max	
$f_{SCL}$	SCL clock frequency	0	320	kHz
$t_{HD;STA}$	Hold time (repeated) START condition	0.6	-	$\mu s$
$t_{LOW}$	Low period of the SCL clock	1.3	-	$\mu s$
$t_{HIGH}$	High period of the SCL clock	0.6	-	$\mu s$
$t_{SU;STA}$	Setup time for a repeated START condition	0.6	-	$\mu s$
$t_{HD;DAT}$	Data hold time	0 <sup>33</sup>	- <sup>34</sup>	$\mu s$
$t_{SU;DAT}$	Data setup time	100		ns
$t_r$	Rise time of both SDA and SCL signals	-	300 (for C = 400pF)	ns
$t_f$	Fall time of both SDA and SCL signals	-	300 (for C = 400pF)	ns
$t_{SU;STO}$	Setup time for STOP condition	0.6	-	$\mu s$
$t_{BUF}$	Bus-free time between a STOP and START condition	1.3	-	$\mu s$
$t_{VD;DAT}$	Data valid time	-	0.9 <sup>34</sup>	$\mu s$
$t_{VD;ACK}$	Data valid acknowledge time	-	0.9 <sup>34</sup>	$\mu s$
$V_{nL}$	Noise margin at the low level	0.1 V <sub>IO</sub>	-	V
$V_{nH}$	Noise margin at the high level	0.2 V <sub>IO</sub>	-	V

Table 19: MAX-M10M I2C peripheral timing and specifications

## 5.3 Default interface settings

Interface	Settings
UART	<ul style="list-style-type: none"> <li>9600 baud, 8 bits, no parity bit, 1 stop bit.</li> <li>Input messages: NMEA and UBX.</li> <li>Output messages: NMEA GGA, GLL, GSA, GSV<sup>35</sup>, RMC, VTG and TXT.</li> </ul>
I2C	<ul style="list-style-type: none"> <li>7-bit I2C address (0x42).</li> <li>Input messages: NMEA and UBX.</li> <li>Output messages: NMEA GGA, GLL, GSA, GSV<sup>35</sup>, RMC, VTG and TXT.</li> </ul>

Table 20: Default interface settings

<sup>33</sup> External device must provide a hold time of at least one transition time (max 300 ns) for the SDA signal (with respect to the min V<sub>ih</sub> of the SCL signal) to bridge the undefined region of the falling edge of SCL.

<sup>34</sup> The maximum  $t_{HD;DAT}$  must be less than the maximum  $t_{VD;DAT}$  or  $t_{VD;ACK}$  with a maximum of 0.9  $\mu s$  by a transition time. This maximum must only be met if the device does not stretch the LOW period ( $t_{LOW}$ ) of the SCL signal. If the clock stretches the SCL, the data must be valid by the set-up time before it releases the clock.

<sup>35</sup> In the default configuration, the NMEA-GSV messages are sent at 5-second intervals to avoid overflow in the TX buffer.

## 6 Mechanical specifications

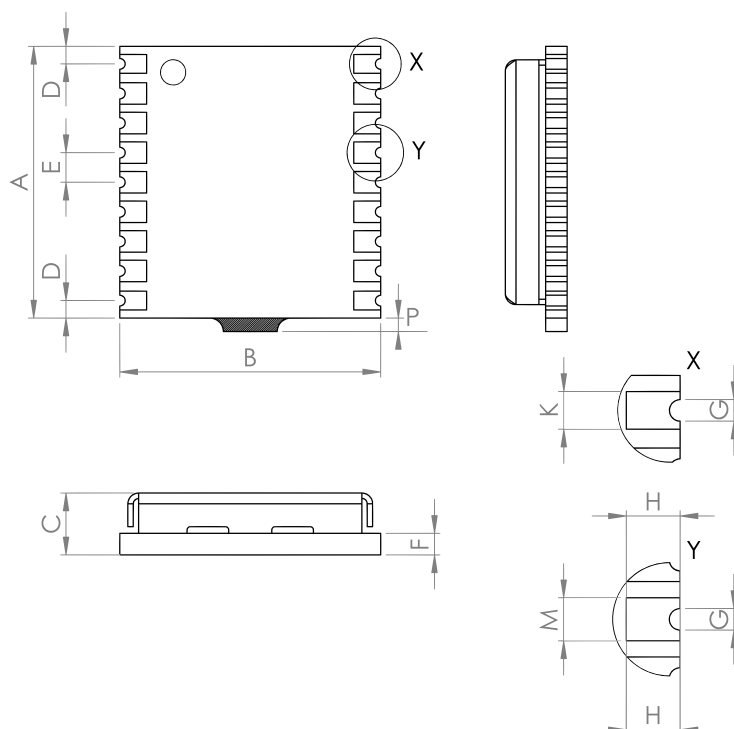


Figure 4: MAX-M10M mechanical drawing



Symbol	Min (mm)	Typical (mm)	Max (mm)
A	10.0	10.1	10.7
B	9.6	9.7	9.8
C	2.2	2.5	2.7
D	0.55	0.65	0.95
E	1.0	1.1	1.2
F	-	0.76	-
G	0.3	0.4	0.5
H	0.9	1.0	1.1
K	0.6	0.7	0.8
M	0.7	0.8	0.9
P	0.0	0.3	0.6
Weight		0.5 g	

Table 21: MAX-M10M mechanical dimensions



The mechanical picture of the de-paneling residual tab (P) is an approximate representation, shape and position may vary.



-  Take the size of the de-paneling residual tabs into account when designing the component keepout area.
-  The pad width (K) applies to all four corner pads.

## 7 Qualifications and approvals

<b>Quality and reliability</b>	
Product qualification	Qualified according to u-blox qualification policy, based on a subset of AEC-Q104
Manufacturing	Manufactured at ISO/TS 16949 certified sites
<b>Environmental</b>	
RoHS compliance	Yes
Moisture sensitivity level (MSL) <sup>36, 37</sup>	4
<b>Type approvals</b>	
European RED certification (CE)	Declaration of Conformity (DoC) is available on the <a href="#">u-blox website</a> .
UK conformity assessment (UKCA)	Yes

**Table 22: Qualifications and approvals**

<sup>36</sup> For MSL standard see IPC/JEDEC J-STD-020 and J-STD-033 [5].

<sup>37</sup> For more information regarding moisture sensitivity levels, labeling, storage, and drying, see the Product packaging reference guide [4].

## 8 Product handling

### 8.1 Soldering

Reflow soldering procedures are described in the IPC/JEDEC J-STD-020 standard [\[5\]](#).

## 9 Labeling and ordering information

This section provides information about product labeling and ordering.

### 9.1 Product labeling

The labeling of MAX-M10M package provides product information and revision information. For more information, contact u-blox sales.

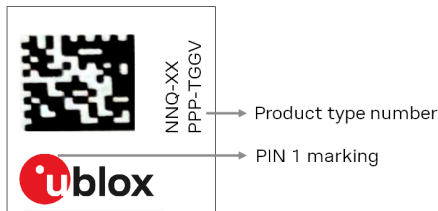


Figure 5: MAX-M10M label

The parts of the product code are explained in [Table 23](#)

Code	Meaning	Example
PPP	Product family	MAX
TGG	Platform	M10 = u-blox M10
V	Variant	M = Standard precision, ROM and XTAL
NN	Option	00, 01, 02, ...
Q	Quality grade	A = Automotive, B = Professional
XX	Product detail	Describes hardware and firmware versions

Table 23: Part identification code

### 9.2 Explanation of product codes

Three product code formats are used in the product label. The product name is used in documentation such as this data sheet and identifies all u-blox products, independent of packaging and quality grade. The ordering code includes options and quality, while the type number includes the hardware and firmware versions.

[Table 24](#) describes the three different product code formats used in the MAX-M10M module.

Format	Structure	Product code
Product name	PPP-TGGV	MAX-M10M
Ordering code	PPP-TGGV-NNQ	MAX-M10M-00B
Type number	PPP-TGGV-NNQ-XX	MAX-M10M-00B-01

Table 24: Product code formats

### 9.3 Ordering codes

Ordering code	Product	Remark
MAX-M10M-00B	u-blox M10 GNSS receiver module, professional grade	

Table 25: Product ordering codes



Product changes affecting form, fit or function are documented by u-blox. For a list of Product Change Notifications (PCNs) see our website at: <https://www.u-blox.com/en/product-resources>.

## Related documents

- [1] MAX-M10M Integration manual, [UBX-22038241](#)
- [2] u-blox M10 SPG 5.10 Interface description, [UBX-21035062](#)
- [3] u-blox M10 SPG 5.10 Release notes, [UBX-22001426](#)
- [4] Product packaging reference guide, [UBX-14001652](#)
- [5] Joint IPC/JEDEC standard, [www.jedec.org](http://www.jedec.org)



For regular updates to u-blox documentation and to receive product change notifications please register on our homepage <https://www.u-blox.com>.

## Revision history

Revision	Date	Status / comments
R01	08-Dec-2022	Advance information.
R02	16-May-2023	Initial production. Changed IN/PCN reference. Updated maximum navigation update rate, sensitivity and BeiDou B1C TTFF in section Performance, ICC_RF in section Absolute maximum ratings, VCC and V_IO supply timing requirements in Operating conditions, and section Mechanical specifications. Added 1 Hz navigation update rate footnote in section Indicative power requirements.
R03	08-Apr-2024	<p>Mass production</p> <p>Added section</p> <ul style="list-style-type: none"> <li>Product handling: Soldering</li> </ul> <p>Updated sections</p> <ul style="list-style-type: none"> <li>Supported GNSS constellations: Supported Assisted GNSS (A-GNSS) services</li> <li>Pin assignment</li> <li>Pin state</li> <li>Absolute maximum ratings: V_IO for VIO_SEL = GND</li> <li>Communication interfaces</li> <li>Mechanical specifications: module weight</li> <li>Qualifications and approvals</li> </ul> <p>Change in document structure</p> <ul style="list-style-type: none"> <li>Moisture sensitivity level (MSL) included in chapter Qualifications and approvals</li> </ul>

## Contact

### **u-blox AG**

Address:      Zürcherstrasse 68  
                 8800 Thalwil  
                 Switzerland

For further support and contact information, visit us at [www.u-blox.com/support](http://www.u-blox.com/support).



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