0x6B -> UPS PIco Module Commands

Address	Name	Size	Туре	R/W	Explanation
0x00	pico_state	Byte	Common	R/W	Write: 0x60 – Set I2C base address to 0x60. Therefore, all parameters will be related to this available base address: 0x68 (RTC and is set then UU), 0x69, 0x6A, 0x6B, 0x6C, 0x6E, 0x6E, 0x6F
					Write: 0x68 – Set I2C base address to 0x60. However, the RTC is free and all related to this register's addresses. Therefore, all parameters will be related to this available base address: 0x69, 0x6B
					Write: 0x50 — Set I2C base address to 0x50. Therefore, all parameters will be related to this available base address: 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5E, 0x5E, 0x5F. User need to take care and change the appropriate Daemon addresses and reload Daemon to have system properly running.
					Write: 0x40 — Set I2C base address to 0x40. Therefore, all parameters will be related to this available base address: 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4E, 0x4E, 0x4F. User need to take care and change the appropriate Daemon addresses and reload Daemon to have system properly running.
					Write: 0xaa – Unconditional File Safe Shutdown and (and Power OFF when battery powered)
					Write: 0xdd - then restore factory defaults. Battery Type will be set according to what has been stored in the original setup. Will stay in the values of 0xdd until factory
					defaults restored, and then will be set to 0x00 Write: 0xee - Reset the UPS PIco CPU, it causes start-up values i.e. RTC will be set to 01/01/2000
					Write: Oxff - Call the UPS PIco Bootloader, Orange Led will be light. Recover from this state can be done only by pressing the RST button, new firmware upload or automatically after 16 seconds if nothing happens. All interrupts are disabled during this procedure. It should be used with RPi Uploading firmware script. Use it very carefully and only when is needed – when firmware uploading. Do not play
					with it; this is not toy functionality. Powering of the pair UPS PIco+RPi must be done via RPi micro USB socket during boot loading process due to following UPS PIco Resets after firmware uploading or when returning from this mode.
					Due to required protection for the RPi from the unconditional reset (files corruption), it is not possible to enter to this mode when system is powered in a different way than in RPI Powering